REMARKS

Claims 1-10 are pending in this application. Claims 1 and 3 have been amended by the present Amendment. No new matter is added by the amendments to claims 1 and 3.

Claims 4, 9 and 10 have been withdrawn from consideration pursuant to 37 C.F.R. § 1.142(b).

CLAIM OBJECTION

Claim 1 has been objected to for the reason stated on page 3 of the Office Action. Applicants have amended claim 1 to remove the informalities. Accordingly, Applicants request that the objection to claim 1 be withdrawn.

REJECTIONS UNDER 35 U.S.C. § 112

Reconsideration is respectfully requested of the rejections of claim 3 under 35 U.S.C. § 112, first and second paragraphs, as failing to comply with the enablement and written description requirements, and as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Applicants have amended claim 3 to remove reference to a connection between the common electrode and the first pixel electrode, and instead claim 3 now recites that the zero electric field is formed by nullifying an electric potential difference between the common electrode and the first pixel electrode.

Accordingly, Applicants request that the Examiner withdraw the rejections of claim 3 under 35 U.S.C. § 112.

REJECTIONS UNDER 35 U.S.C. § 103(a)

Reconsideration is respectfully requested of the rejection of (1) claims 1-3, 5 and

6 under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent Application Pub. No. 2002/0113931 ("Park") in view of U.S. Patent No. 5,617,230 ("Ohgawara"); and (2) claims 7 and 8 under 35 U.S.C. § 103(a) as being unpatentable over Park in view of Ohgawara, and further in view of U.S. Patent No. 5,101,289 ("Takao").

Claim 1 recites, *inter alia*, that a gate electrode of the thin film transistor is connected to ground so that a data line is electrically disconnected from the first pixel electrode.

For example, referring to paragraph 0074 of Applicants' disclosure, the gate electrode of the thin film transistor of the pixel corresponding to the border area is not connected to the gate line, and is instead connected to ground. As a result, the thin film transistor 114 is turned off, and the image data outputted from the data line is not transmitted to the pixel electrode 112.

In contrast to the claimed embodiment, none of the cited references disclose connecting the gate electrode of a border area TFT to ground.

Accordingly, Applicants respectfully submit that none of the cited references, when taken alone or in combination, disclose or suggest the claimed embodiment, and there is no motivation to develop same.

Therefore, Applicants respectfully submit that claim 1, and claims 2-3 and 5-8, which depend from claim 1, are patentable over the cited references.

As such, Applicants respectfully request that the Examiner withdraw the rejections of claims 1-3 and 5-8 under 35 U.S.C. § 103(a).

An early and favorable reconsideration is earnestly solicited. If the Examiner has any further questions or comments, the Examiner may telephone Applicants' Attorney to reach a prompt disposition of this application.

Respectfully submitted,

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